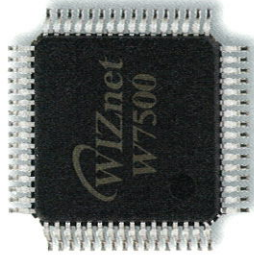


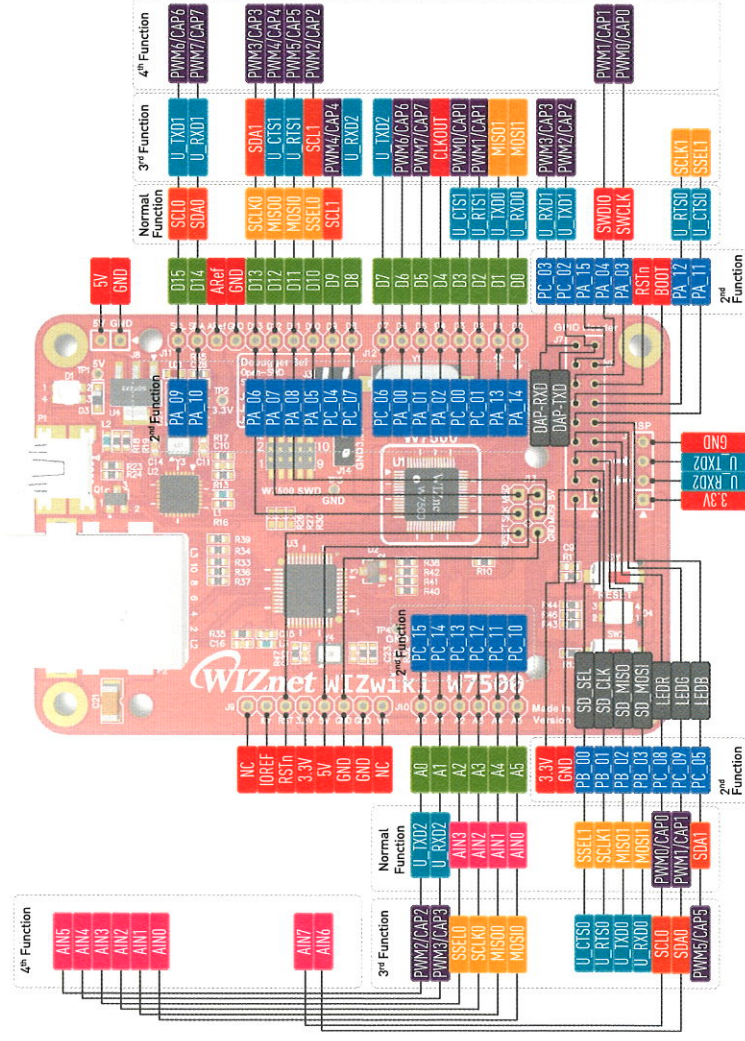
W7500

IOP(Internet Offload Processor) for IoT



- **ARM Cortex-M0**
 - 48MHz max. frequency
- **Un-attackable Hardwired TCP/IP Core**
 - 8 Sockets
 - SRAM for socket: 32 KB
 - MII (Medium-Independent Interface)
- **Memories**
 - Flash: 128 KB
 - ROM for boot code: 6 KB
- **Clock, reset and supply management**
 - POR (Power-On Reset)
 - Internal Voltage Regulator : 3.3V to 1.5V
 - 8-to-24MHz external crystal oscillator
 - Internal 8MHz RC Oscillator
 - PLL for CPU clock
 - 32 kHz oscillator for RTC
- **ADC**
 - 12bit, 8ch, 1MSPS
- **DMA**
 - 6-channel DMA controller
 - Peripheral supported: UARTs, SPIs
- **GPIO**
 - 53 I/Os (16 IO x 3ea, 5 IO x 1ea)
- **Debug mode : Serial Wire Debug (SWD)**
- **Timer/PWM**
 - 1 Watchdog (32-bit down-counter)
 - 4 Timers (32-bit or 16-bit down-counter)
 - 8 PWMs
- (32-bit counter/timers with programmable 6-bit prescaler)
- 1 RTC (calendar, alarm)
- **Communication Interfaces**
 - 3 UART
 - (2 UARTs with FIFO and Flow Control, 1 simple UART)
 - 2 SPI
 - 2 I2C (Master/Slave, Fast-mode (400 kbps))
- **Crypto**
 - 1 RNG (Random Number Generator)
 - : 32-bit random number
- **Package : 64 TQFP (7x7 mm)**

WIZwiki™ W7500



- mbed enabled & Arduino compatible
- Extension pin headers for full access to all W7500 I/Os
- IOP W7500 chip on-board for IoT

- Get more detail from wizwiki.net

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