Table of Contents

How to make W7500 Keil Project	. 1
Introduction	. 1
STEP 1. Download and install KEIL5!	. 1
STEP 2. Download W7500 Library!	. 1
STEP 3. make workspace! and move the W7500 Library to workspace!	. 2
STEP 4. Execute KEIL5 and make new project!	. 2
STEP 5. W7500 start up code and system code setting!	. 4
STEP 6. setting for include paths	8
STEP 7. Memory setting!	10
STEP 8. Selection of Peripherals to use	13
STEP 9. Setting user program to use with the created bin file	14
STEP 10. Compile the example	15
•	19

Last update: 2015/05/04 20:37

How to make W7500 Keil Project

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- author : IOP Team
- version : V1.0.0
- date : 1-May-2015
- brief : Description of making a new Keil Project of W7500.
- develop environment : Windows 7 32/64bits, Keil Version 5.10

Introduction

This application note is a tutorial for making a KEIL project for W7500 MCU. This document show you how to make a project and a bin file for W7500 step by step in order for you to follow up easily.

STEP 1. Download and install KEIL5!



- Download MDK-ARM v5 : KEIL5
- Installation instructions, please see the Link : Install

STEP 2. Download W7500 Library!

Document Wiki Site



You can download W7500 Library from WIZnet's Github repository : W7500 Library

STEP 3. make workspace! and move the W7500 Library to workspace!

Comput	er ► Local Disk (D:) ► workspace ►	project •		
Favorites	Name	Date modified	Туре	Size
🔜 Desktop ᠾ Downloads 🕮 Recent Places	Libraries Drojects Utilities	2015-04-28 오후 2: 2015-04-28 오후 2: 2015-04-28 오후 2:	File folder File folder File folder	

In this example, it is D:\workspace\project\Library

STEP 4. Execute KEIL5 and make new project!

🐺 μVision	-								
File Edit View	Pro	ject Flash Debug	Peripherals	Tools :	svcs	Window	Help		
i 🗅 💕 🖌 🥔		New µVision Project.							
		New Multi-Project W	orkspace						
	1	Open Project							
Project		Save Project in µVisio	on4 format						
		Close Project							
		Export							•
		Manage							•
		Select Device for Targ	get						
		Remove Item							
	N.	Options							Alt+F7
		Clean targets							
		Build target							F7
		Rebuild all target file	25						
		Batch Build							
		Translate							Ctrl+F7
		Stop build							

Click to New uVision Project...

😨 Create New Project					×
Computer > Loc	al Disk (D:) 🕨 workspace 🕨 project 🕨 W7500)_Test	• 4)	Search W7500_Test	Q
Organize 🔻 New folder					= • 🕐
Desktop A Name Downloads	e Dat	e modified Type No items match your search.	Size		
Carlies Cocuments Carlier					
Local Disk (C:)					
File <u>n</u> ame: W7500_Test					•
Save as type: Project Files (*.	uvproj; *.uvprojx)				
Hide Folders				Save	Cancel

Select a project save folder and choose project name. In this example, D:\workspace\project\W7500_test\W7500_Test.uvproj

Options for Target 'Target 1'		×
Device Target Output Listing	g User C/C++ Asm Linker Debug Utilities	
Generic CPU D	Data Base	
Vendor: ARM		
Device: Cortex-M0		
Toolset: ARM		
Search:		
🕀 🔗 Analog Devices	Cortex-M0 based Microcontroller	*
🖃 🖗 ARM		
Cortex-M0		
Cortex-M0 SDK		
Cortex-M0+		
Cortex-M1		
Cortex-M1 (Altera)		
Cortex-M3		
C Cortex-M4		-
Cortex-M4 FPU	▼	•
	OK Cancel Defaults	Help

Choose 'Cortex-M0' because W7500 MCU is based on Cortex-M0

STEP 5. W7500 start up code and system code setting!



Return to project. You need to make the same folders like the image which is shown on right side



Close

•

Files of type: C Source file (*.c)

Close

•

Document Wiki Site

- http://wizwiki.net/wiki/

Files of type: C Source file (*.c)

Look in: 🎉 Source	- 🗢 🖻 🗝 -		Look in: 🍑 ARM	- 🗢 🖻 -
Name	Date modified	Туре	Name	Date modified Type
ARM	2015-04-28 오후 2:22	File folder		No items match your search.
🌽 G++	2015-04-28 오후 2:22	File folder		
📕 GCC	2015-04-28 오후 2:22	File folder		
JAR IAR	2015-04-28 오후 2:22	File folder		
system_W7500x.c	2015-04-27 오후 4:24	C File	->	
< [m	•		····
ile <u>n</u> ame:		Add	File name:	Add
iles of type: C Source file (*.c)	•	Close	Files of type: C Source file (*.c)	▼ Close
			C Source file (".c)	
		1	Asm Source file (* s Object file (* obj)	°.°src.°.a°)
			Object nie (.obj)	
		/	\rightarrow	
		× /		
			No. No.	
Add Files to Group 'MDK-ARM'			Project	Q
Add Files to Group 'MDK-ARM'	- ← € ☆ 💷 -		Project	¢ (
Add Files to Group 'MDK-ARM'	▼ 🗢 🖻 💏 📰▼ Date modified	Туре	Project	¢.

Name	Date modified	Туре	E Target 1
startup_W7500x.s	2015-04-27 오후 4:24	S File	D MDK-ARM
< II		,	Startup_W7500x.s
File name: startup_W7500x.s Files of type: Asm Source file (".s", ".src; ".a")		Add Close	- W7500_Periphs
			User

Follow like upper images.



And in the same way... CMSIS Folder include D:\workspace\project\Libraries\CMSIS\Device\WIZnet\W7500\Source\system_W7500.c file

W7500_Periphs folder includes Peripherals to use.

And User folder has to include main.c and so on.

Let's make main.c

2015/06/14 02:18



Click to Add New Item to the Group.

Add New Item to Group 'Us		x
C File (,c) C ++ File (,cpp) A Asm File (,s) Header File (,h) Text File (,txt) Image File (,*) User Code Templat	Create a new C source file and add it to the project.	
Type: C File (.c) Name: main Location: D:₩works	ace₩project₩W7500_Test	
	Add Close Help	

Select C file and Name it with 'main' and click 'Add'.

D:\workspace\project\W7500_Test\W7500_Te	est.uvproj - µVision
<u>File Edit View Project Flash Debug Perij</u>	pherals <u>T</u> ools <u>S</u> VCS <u>W</u> indow <u>H</u> elp
🗋 🗋 🚰 🛃 🖉 🕹 🏝 🖀 🖌 🗠 🦛	-> 🏴 隐 隐 殷 律 津 //』 //漫 🖄 exti
🛿 🥙 🍱 🥔 🔜 🛛 🗱 🛛 Target 1	🕞 🔊 📥 🕾 🗇 🎯
Project 🛛 🗣 🔯	main.c*
Target 1 MDK-ARM Startup_W7500x.s CMSIS W7500_Periphs W7500_Periphs Main.c	× 1 #include "w7500x.h"

Include **w7500x.h** header but... you should show red X because you did not path.

STEP 6. setting for include paths



Click to Options for Target...

Options for Target 'Target 1'	Options for Target 'Target 1'
Device Target Output Listing User C/C+++ Asm Linker Debug Utilities	Device Target Output Listing User C/C++ Asm Linker Debug Utilities
Preprocessor Symbols	Preprocesses Combole Folder Setup
Define:	
Undefine:	Undefin Setup Compiler include Paths:
Language / Code Generation	Languas
Qptimization: Level 0 (-00) ▼ Enum Container always int	Qptimizat
Coptimize for Time	C Optir
Split Load and Store Multiple Read-Only Position Independent No Auto Includes	∏ <u>S</u> pit
One ELF Section per Function Read-Write Position Independent C99 Mode	
Include Teaths	Include Path
Misc Controls	Mist Control
Compiler c -cpu Cottex:M0-D_EVAL-H g -00 -apcs=interwork control -1 C:\#Kei_v5\#ARM\#RV31\#INC *	Compile OK Cancel
OK Cancel Defaults Help	OK Cancel Defaults Help

Select 'Include Paths' in C\C++ tap and click the folder icon and click to ... icon

Jevice Target Outp	out Listing User C/C++ Asm Linker Debug Utilities
Preprocessor Symbols	Browse for Folder
Define:	Select Folder:
Undefine: Setup	> Comp D:₩workspace₩project₩Libraries₩CMSIS₩Device₩WIZne
Language /	⊿ 🎉 workspace
Ontimization	a broject
	CMSIS
	A Device
C One ELE	a 🕌 WIZnet 🗉
1 ONO ECI	⊿ <mark>} w7500</mark>
Include	Include III
Misc I	b source
Controls	
Compiler	OK Carrel
string	
- ,	C

Preprocessor Symbols -	Browse for Folder
Define: Folder Se Undefine: Setup C Language / Optimization:	Select Folder: amp D: Wworkspace Wproject WLbraries WW7500x_stdPerph_Driv Wworkspace project project CMSIS
Solt Load	W7500c stdPerph_Driver F Projects Utilities W7500_Test
Compiler control	OK Cance



Set 'include path' with

D:\workspace\project\Libraries\CMSIS\Device\WIZnet\W7500\Include D:\workspace\project\Libraries\W7500x_stdPeriph_Driver\inc D:\workspace\project\Libraries\CMSIS\Include

vice Target Output Listing Us	er C/C++ Asm Linker Deb	ug Utilities
Preprocessor Symbols		
Define:		
Undefine:		
Language / Code Generation		9.57
	Strict ANSI C	Warnings:
Optimization: Level 0 (-00) -	Enum Container always int	<unspecified></unspecified>
Optimize for Time	Plain Char is Signed	🗖 Thum <u>b</u> Mode
Split Load and Store Multiple	Read-Only Position Independent	t T No Auto Includes
One ELF Section per Function	Eead-Write Position Independer	nt 🗌 C99 Mode
Include Paths <u>Misc</u> Controls	ice₩WIZnet₩W7500₩include;₩⊔brarik	ss₩W7500x_stdPeriph_Driver
Compiler control string -c -cpu Cortex-M0 -D_EVA ₩W7500₩Include -I₩Lib	∖L -ili-g-00apcs=interwork -l₩Libraries ¹ raries₩W7500x_stdPeriph_Driver₩inc	#CMSIS₩Device₩WIZnet



STEP 7. Memory setting!

2015/06/14 02:18

11/20

D:\workspace\project\W7500_Test\W7500_Test	t.uvproj - μVision
<u>File Edit View Project Flash Debug Periph</u>	nerals <u>T</u> ools <u>S</u> VCS <u>W</u> indow <u>H</u> elp
🗋 🗋 🚰 🛃 🕷 🕹 🛍 🗳 🗠 🖛	隆 🏦 🦍 🚷 葦 🏥 🎼 🙆
🕸 🎬 🕮 🥪 🔜 🛛 🗤 🕅 Target 1	- 🛃 📥 🖘 🚳
Project Target 1 MDK-ARM Startup_W7500x.s CMSIS W7500_Periphs User Main.c	Configure target options

Click "Options for Target..."

Options for Target 'Target 1'	×
Device Target Output Listing User C/C++ A	sm Linker Debug Utilities
ARM Cortex-M0	Code Generation
Operating system: None	Use Cross-Module Optimization
System-Viewer File (.Sfr):	🗆 Use MicroLIB 🔲 Big Endian
Use Custom SVD File	
Read/Only Memory Areas	Read/Write Memory Areas
default off-chip Start Size Startup	default off-chip Start Size NoInit
ROM1: C	□ RAM1: □
□ ROM2: ○	□ RAM2: □
□ ROM3: ○	□ RAM3: □
on-chip	on-chip
□ IROM1: 0	□ IRAM1: □ □
□ IROM2: 0	□ IRAM2: □
OK Car	ncel Defaults Help

👿 Options for Target 'Target 1'	×
Device Target Output Listing User C/C++ A	sm Linker Debug Utilities
ARM Cortex-M0 <u>X</u> tal (MHz): 12.0	Code Generation
Operating system: None	Use Cross-Module Optimization
System-Viewer File (.Sfr):	T Use MicroLIB T Big Endian
Use Custom SVD File	
Read/Only Memory Areas	Read/Write Memory Areas
default off-chip Start Size Startup	default off-chip Start Size Nolnit
□ ROM1: C	□ RAM1: □
□ ROM2: □ C	□ RAM2: □
□ ROM3: □ C	RAM3:
on-chip	on-chip
IROM1: 0x0 0x20000 €	IRAM1: 0x20000000 0x4000 □
IROM2:	IRAM2:
OK Can	cel Defaults Help

Click "Target" tap and set with the values which are shown in above pictures

Options for Target 'Target 1'	x
Device Target Output Listing User C/C++ Asm	Linker Debug Utilities
Use Memory Layout from Target Dialog Make RW Sections Position Independent Make RO Sections Position Independent Don't Search Standard Libraries Report 'might fail' Conditions as Errors	X/O Base:
Scatter File	E dit
Misc controls	×
Linkercpu Cortex-M0 *.o controlstrictscatter "W7500_Test.sct" string	*
OK Cance	el Defaults Help

And click "Linker" tap and check [Use Memory Layout from Target Dialog]

STEP 8. Selection of Peripherals to use

D:\workspace\project\W7500_Test\W7500_Tes	t.uvproj - µVision
<u>File Edit View Project Flash Debug Perip</u>	herals <u>T</u> ools <u>S</u> VCS <u>W</u> indow <u>H</u> elp
📄 🗅 😅 🔙 🥔 🕉 🐜 隆 🤟 🗠 🦛 =	🕅 🎁 🏭 🥵 🦉 👘 🖉
🕸 🏥 🕮 🥪 🔜 🛛 🗱 🛛 Target 1	🛃 🛣 🖶 🗇 🍏
Project Target 1 MDK-ARM Startup_W7500x.s CMSIS W7500_Periphs User Main.c	Configure target options of

Click "Options for Target..."

Last

update: 2015/05/04 products:w7500:documents:appnote:howtokeil http://wizwiki.net/wiki/doku.php?id=products:w7500:documents:appnote:howtokeil 20:37

Options for Target 'Target 1'		×
Device Target Output Listi	ng User C/C++ Asm Linker Debug	Utilities
Preprocessor Symbols		
Define: CORTEX_M0 USE_	STDPERIPH_DRIVER	
U <u>n</u> define:		
Language / Code Generation —		Marriana
	Strict ANSI C	<u>vv</u> amings:
Optimization: Level 0 (-00)	Enum <u>C</u> ontainer always int	<unspecified></unspecified>
Optimize for Time	Plain Char is Signed	🗖 Thum <u>b</u> Mode
Split Load and Store Multiple	Read-Only Position Independent	No Auto Includes
One ELF Section per Function	on <u>R</u> ead-Write Position Independent	C99 Mode
Include Paths Misc Controls Compiler control string	SWDeviceWWIZnetWW7500WInclude;WLibrariesW DEVAL -li -g -O0apcs=interwork -1WLibrariesWCI -1WLibrariesWW7500x_stdPeriph_DriverWinc	#W7500x_stdPeriph_Driver MSIS₩Device₩WIZnet
	OK Cancel Defaults	Help

Click "C/C++" tap and add CORTEX_M0 USE_STDPERIPH_DRIVER in "Define" field.

STEP 9. Setting user program to use with the created bin file

D:\workspace\project\W7500_Test\W7500_Te	st.uvproj - μVision
<u>File Edit View Project Flash Debug Perip</u>	herals <u>T</u> ools <u>S</u> VCS <u>W</u> indow <u>H</u> elp
🗋 🗁 🛃 🥔 🕺 🖦 🛍 🥙 (~)	- 隆 🏨 🎊 🎊 🎼 🎼 🎼 🏙
🛛 🥸 🎬 🥔 🔜 🛛 💥 🛛 Target 1	🕒 🕹 着 🗟 😒
Project 🛛 📮 💌	Options for Target
⊡	Configure target options
⊡ ← MDK-ARM	
startup_w/500x.s	
En CIVISIS	
W7500 Periphs	
main.c	

Click "Options for Target..."

Options for Target 'Target 1'	×
Device Target Output Listing User C/C++ Asm Linker Debug Utilities	
Run User Programs Before Compilation of a C/C++ File	
Run #1:	🗖 DOS16
Stop Build/Rebuild #1 on Exit Code: Not Specified	
Stop Build/Rebuild #2 on Exit Code: Not Specified	
Run User Programs Before Build/Rebuild	
Run #1:	🗖 DOS16
□ Run #2:	DOS16
Run User Programs After Build/Rebuild	
Run #1	🗖 DOS16
□ Run #2:	🗖 DOS16
Image: Beep When Complete Image: Start Debugging	
OK Cancel Defaults	Help

Click "User" tap and check "Run #1" in "Run User Programs After Build/Rebuild" section and write this command **[fromelf --bin -o "\$L@L.bin" "#L"]** in order CMSIS DAP to use the bin file.

STEP 10. Compile the example

Compu	iter → Local Disk (D:) → workspace → pro	ject > W7500_Test		er → Local Disk (D:) → workspace →	project 🕨 Projects 🕨 Peripheral Ex	amples + GPIO	▶ Blink LED ▶
Organize 🔻 Include	in library 🔻 Share with 💌 Burn	New folder	Organize 👻 👪 Edit	Burn New folder			
🔆 Favorites	Name	Date modified	🔆 Favorites	Name	Date modified	Туре	Size
🧮 Desktop	main.c	2015-04-30 오전 8:	🧮 Desktop	GCC	2015-04-28 오후 2:	File folder	
鷆 Downloads	W7500_Test.build_log.htm	2015-04-30 오전 8:	🐌 Downloads	MDK	2015-04-30 오전 8:	File folder	
🔛 Recent Places	W7500_Test.uvproj	2015-04-30 오전 8:	📃 Recent Places	📋 main.c	2015-04-27 오후 4:	C File	5
		1		* readme.md	2015-04-28 오전 1	MD File	3
🥽 Libraries			ز Libraries	W7500x_conf.h	2015-04-27 오후 4:	H File	3
Documents			Documents	W7500x_it.c	2015-04-27 오후 4:	C File	7
Git Git			Git	W7500x_it.h	2015-04-27 오후 4:	H File	3
J Music		Co	Husic				

Let's compile the example code in

D:\workspace\project\Projects\Peripheral_Examples\GPIO\Blink_LED This fold consists of main.c, W7500x_conf.h, W7500x_it.c and W7500x_it.h and you should copy these four files to D:\workspace\project\W7500_Test(my project folder)

Tarnet 1			1 日/**						
MDK-ARM	W750	0x.s	2 * @fi 3 * @fi 4 * @a 5 * @v 6 * @v	ile GPI author IC version V date 01	D/E I/P 1.0.				
	Nº SO	Options for Group 'W750	7 + @k	Alt+F7		dd Files to Group 'W7500_Perij	phs'		
🖻 📇 User	575 -	Add New Item to Group '	N7500 Periphs'	E	NT Loo	k in: 🕕 src	-	← 🗈 💣 🗊 -	
0074		Add Existing Files to Grou	p W7500 Periphs'.		NG Na	me Desktop		Date modified	Туре
		Add Group		NI.		W75 CLibraries		2015-04-27 오후 4:24	C File
		Remove Group 'W7500_Pe	riphs' and its Files	N	=OF 🛛	W75 🧸 Admin		2015-04-27 오후 4:24	C File
		Manage Project Items			128	W75 Computer		2015-04-27 오후 4:24	C File
	-			-		W75 Local Disk (C:)		2015-04-27 오후 4:24	C File
		Open File				W75 workspace		2015-04-27 오후 4:24	C File
		Open List File		-	*	project			1
		Open Map File		5	00x File	jame			Add
		Open Build Log		e	def Files	of typ	dPeriph_Driver		Close
		Rebuild all target files		fir	ne ·	W7500_Test		_	
		Build target		F7 10	ro	SAMSUNG (F:)			
		Translate File		ri:	able	-			
	110	Stop build		R	Def				
	V	Show Include File Depend	encies	ic s	tior (

And copy W7500x_gpio.c into W7500_Periphs folder in order to use gpio peripheral

Look in:	W7500_Test	← 🗈 💣 📰▼		
Name	*	Date modified	Туре	
main.c		2015-04-27 오후 4:	C File	
W7500x	it.c	2015-04-27 오후 4:	C File	
4	III			ļ
			_	Add
File <u>n</u> ame:	"main.c" "W7500x_it.c"			

And copy W7500x_it.c into User folder for interrupt handlers

Add "Include Path" for W7500x_conf.h and W7500x_it.h

17/20



Click "Options for Target..."

Options for Target 'Target 1'		Options for Target 'Target 1'
Device Target Output Listing User C7C+++ Asm Linker Debu	g Utilities	Device Target Output Listing User C7C++ Asm Linker Debug Utilities
Preprocessor Symbols		Preprocesses Sumbals Polder Setup Defin Undefin Setup Compiler Include Paths:
Language / Code Generation Strict ANSI C Qptimization: Level 0 (-00) Container always int Optimize for Time Split Load and Store Multiple Cone ELF Section per Function Bead-Write Position Independent	Wamings: anspecified> Thumb Mode No Auto Includes C 99 Mode	Cotimizar Coptinizar Coptin Split One
Include Paths Controls Controls Compiler control - C-cpu Cortex-M0-D_EVAL-#.g-00-apcs=interwork control - C-WKel_v5WARMWRV31WINC		Include Path Mase Control Comple String
OK Cancel Default	s Help	OK Cancel Defaults Help

Click "Include Paths" in C\C++ tap, "folder icon and ... icon



Set "include path" with D:\workspace\project\W7500_Test

D:\#workspace\#project\#W7500_Test\#W7500_Test.uvproj - \u00c4Vision					
File Edit View Project Flash Debug Peri	pherals Tools SVCS Window Help				
🗋 📴 🛃 🗿 🔉 🖏 🛍 🗠 🗠 🦛	→ 🏞 穆 穆 穆 傳 淳 //注 /版				
🛛 🥙 🛅 🎬 🥪 🔜 🛛 🕍 🕅 Target 1	💽 🔊 📥 🗟 🗇 🎯				
Project Build (F7) 4 Build target files	1 □ /** 2 *********************************				
CMSIS CMSIS W7500_Periphs W7500x_gpio.c User Main.c W7500x_it.c	4 * @author IOP Team 5 * @version V1.0.0 6 * @date 01-May-201! 7 * @brief Main program 8 ************************************				

Now compile this project!! by pressing F7 or clicking Build icon

19/20

B:#workspace#project#W7500_Test#W7500_Test	tuvproj - µVision		
File Edit View Project Flash Debug Perip	nerals Tools SVCS Window Help		
□ 2 2 3 3 4 8 9 9 4 4 4	陀 熟 熟 後 詳 詳 /// /版 🎯 📃 🖬 🐼 🧶 🔍 🔺 🔍 🔍		
🛛 🏵 🏝 🧼 🤤 🔤 🔤 Target 1	🗷 🔊 📩 🗟 🧇 🎯		
Project 🛛 🗣 🔝	i main.c	▼ ×	
Target 1 MDK-ARM MDK-ARM System_W7500x.s GMSIS W7500_Periphs W7500_Periphs W7500_gpio.c Juser Wr500x_gpio.c Wr500x_it.c	1 B/** * @file GPIO/Blink_LED/main.c * @author IOP Team * * @version V1.0.0 * * @date 01-May-2015 * @brief Main program body * @attention * * THE PRESENT FIRMWARE WHICH IS FOR GUIDANCE ONLY AIMS AT PROVIDING CUSTOMERS * WITH CODING INFORMATION REGARDING THEIR PRODUCTS IN ORDER FOR THEM TO SAVE * WITH CODING INFORMATION REGARDING THEIR PRODUCTS IN ORDER FOR THEM TO SAVE * WITH CODING INFORMATION CONTAINED AMAGES WITH RESPECT TO ANY CLAIMS ARISING * FROM THE CONTENT OF SUCH FIRMWARE AND/OR THE USE MADE BY CUSTOMERS OF THE * CODING INFORMATION CONTAINED HEREIN IN CONNECTION WITH THEIR PRODUCTS. * (h2> <center>© COPYRIGHT 2015 WIZnet Co.,Ltd. * (h2><center>© COPYRIGHT 2015 WIZnet Co.,Ltd. * (h2><center>© COPYRIGHT 2015 WIZnet Co.,Ltd. * (h2><center> * Private typedef */ */ */ */ */ */ */ */ */ */ */ */ */ */</center></center></center></center>		
Project Books { Functions 0. Templates	31 /* Private variables*/		
Ruild Output	,		
Build target 'Target 1' Build target 'Target 1' assembling startup W7500x.s compiling W7500x_gpio.c compiling W7500x_it.c compiling W7500x_it.c linking Program Size: Code=2160 RO-data=200 RW-data=16 ZI-data=864 After Build - User command #1: fromelfbin -o "D:\workspace\project\W7500_Test\W7500_Test.bin" "D:\workspace\project\W7500_Test\W7500_Test\W7500_Test.W7500_Test.W7500_Test\W7500_Test\W7500_Test\W7500_Test\W7500_Test.W7500_Test\W7500_Test\W7500_Test\W7500_Test\W7500_Test\W7500_Test.W7500_Test\W7500_Test\W7500_Test\W7500_Test\W7500_Test.W7500_Test\W7500_Test			
•	III.	•	
	Simulation	L:19 C:26	

After compile, **W7500_Test.bin** file will be created. And then WIZwiki-W7500 will upload this bin file on itself via the User program you registered.



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