

Design Tip with W3100A for W3100 Users

- Different pin definition W3100 vs. W3100A

Pin	W3100	W3100A
4	NC (Not Connected)	CLOCK
28	NC (Not Connected)	MODE[2]
33	NC (Not Connected)	EXT_CLK
39	NC (Not Connected)	VCC
47	NC (Not Connected)	MODE[1]
56	NC (Not Connected)	MODE[0]
59	NC (Not Connected)	SCL
60	NC (Not Connected)	SDA

- If you already released PCB targeted W3100, following pin reconfiguration process is required for replacing W3100 with W3100A.

1. Set pin No.39 to VCC
2. Input 25Mhz clock to pin No.4
3. If you used fast CPU and CS (Chip Select signal for W3100) is designed to stay active low for shorter than 80 ns (access time), please set the operating mode to Non-clocked mode with mode pin reconfiguration as follow:

MODE[2]->0, MODE[1]->1, MODE[0]->0

Otherwise (for default mode), leave MODE[2:0] unconnected or set to GND.

4. And you'd better set other pins(33,59,60) to GND.